

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

ATTY. DOCKET NO.

SERIAL NO.

10003881-1

09/991,020

APPLICANT

Christopher K. Sutton

FILING DATE

11/15/2001

GROUP

2858

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)



REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	*	DOCUMENT NUMBER	DATE	NAME
SP		5,390,131	Feb. 14, 1995	Rohrbaugh, John G. et al
SP		6,304,095	Oct. 16, 2001	Miyamoto, Takashi

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

SP	G. Freeman, J. Kierstead, W. Schweiger, "Electrical Parameter Data Analysis and Object-Oriented Techniques in Semiconductor Process Development", Proceedings of the 1996 BIPOLAR/BICMOS Circuits and Technology Meeting, IEEE, 29 September 1996 (1996-09-29) - 1 October 1996 (1996-10-01), pages 81-88, XPO02258074

EXAMINER

Emerson Puente

DATE CONSIDERED

5/25/04

* Copies of these references are not enclosed pursuant to 37 CFR 1.98(d). (See accompanying IDS)

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